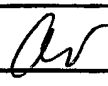
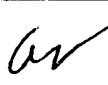

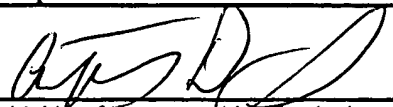




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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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		DOCUMENT NUMBER	ISSUE DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	I						
	J						
	K						
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	L	A. Krymski et al.; "A high speed, 500 frames/s, 1024 X 1024 CMOS active pixel sensor;" Proceeding of the 1999 Symposium on VLSI Circuits, pp. 137-138, Jun. 1999					
	M	N. stevanovic et al.; "A CMOS image sensor for high speed imaging;" Digest of Technical papers of the 2000 IEEE International Solid-State Circuits Conference, pp. 104-105, Feb. 2000					
	N	R. L. Lagendijk et al.; "Maximum likelihood image and blur identification: a unifying approach;" Opt. Eng., Vol. 29, No. 5, pp. 422-435, May 1990					
EXAMINER  DATE CONSIDERED 1/4/2006							
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Ar	A	5 2 7 2 5 3 5	12/21/93	Elabd	358	213.11	6/13/91
Ar	B	5 4 6 1 4 2 5	10/24/95	Fowler et al.	348	294	2/15/94
Ar	C	5 5 8 3 3 6 7	12/10/96	Blossfeld	257	426	1/17/95
Ar	D	5 7 4 2 0 4 7	4/21/98	Buhler et al.	250	214	10/1/96
Ar	E	5 8 0 1 6 5 7	9/1/98	Fowler et al.	341	155	2/5/97
Ar	F	5 8 4 1 1 2 6	11/24/98	Fossum et al.	250	208.1	1/24/975
Ar	G	5 9 0 0 6 2 3	5/4/99	Tsang et al	250	208	8/11/97
Ar	H	5 9 6 9 7 5 8	10/19/99	Sauer et al.	348	241	6/2/97
Ar	AA	6 0 7 8 0 3 7	6/20/00	Booth, Jr.	250	208.1	4/16/98
Ar	BB	6 1 3 0 4 2 3	10/10/00	Brehmer et al.	250	208.1	7/10/98
Ar	CC	6 1 5 7 0 1 6	12/5/00	Clark et al.	250	208.1	9/30/97

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	ISSUE DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	I							
	J							
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Ar	L	Hon-Sum Wong; "Technology and device scaling considerations for CMOS imagers;" IEEE TRANSACTIONS ON ELECTRON DEVICE, VOL. 43, NO. 12, DECEMBER 1996
Ar	M	D. Yang et al.; "Comparative analysis of SNR for image sensors with enhanced dynamic range;" SPIE, EL 1999
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